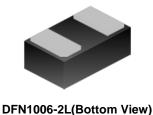


# Bi-directional 4.5V High Capacitance ESD Protector

#### **Description**

The PESDHC2FD4V5BH protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



#### **Feature**

- $\triangleright$  500W peak pulse power per line ( $t_P = 8/20\mu s$ )
- DFN1006-2L package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns</p>
- Low clamping voltage
- RoHS compliant
- > Transient protection for data lines to

IEC61000-4-2(ESD) ±30KV(air), ±30KV(contact);

IEC61000-4-4 (EFT) 40A (5/50ns)

IEC61000-4-5 (Surge) 36A (8/20us)

# Pin 1 Pin 2 Circuit Diagram

### **Applications**

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

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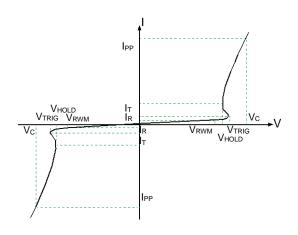
Marking (Top View)

## **Mechanical Characteristics**

- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- DFN1006-2L without plating

## **Electronics Parameter**

Symbol	Parameter		
V <sub>RWM</sub>	Peak Reverse Working Voltage		
I <sub>R</sub>	Reverse Leakage Current @ V <sub>RWM</sub>		
V <sub>TRIG</sub>	Reverse trigger Current		
VHOLD	Reverse holding voltage		
lτ	Test Current		
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P <sub>PP</sub>	Peak Pulse Power		
CJ	Junction Capacitance		
lF	Forward Current		
VF	Forward Voltage @ I <sub>F</sub>		



# **Electrical characteristics per line@25℃ (unless otherwise specified)**

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V <sub>RWM</sub>				4.5	V
Reverse trigger voltage	$V_{TRIG}$	I <sub>TRIG</sub> =2uA	4.7			V
Reverse holding voltage	VHOLD	I <sub>HOLD</sub> =50mA	4.6			
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 4.5V T=25°C			1.0	μΑ
Maximum Reverse Peak Pulse Current	l <sub>PP</sub>			36		Α
Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> =16A t <sub>p</sub> =100ns		6		V
Clamping Voltage	Vc	I <sub>PP</sub> =1A t <sub>P</sub> = 8/20µs		4.8	5.8	V
Clamping Voltage	Vc	I <sub>PP</sub> =20A t <sub>P</sub> = 8/20μs		10	13	V
Clamping Voltage	Vc	$I_{PP}=36A$ $t_{P}=8/20\mu s$		14	18	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> =0V f = 1MHz	70	80	90	pF

# Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t <sub>p</sub> =8/20μs)	P <sub>pp</sub>	500	W
Operating Temperature	TJ	-55 to 150	$^{\circ}$
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C

# **Typical Characteristics**

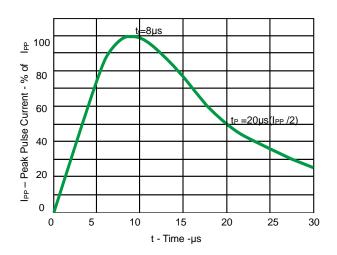


Fig 1.Pulse Waveform

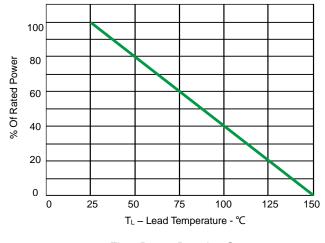


Fig 2.Power Derating Curve

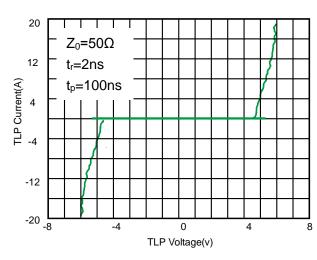


Fig 3.TLP Measurement

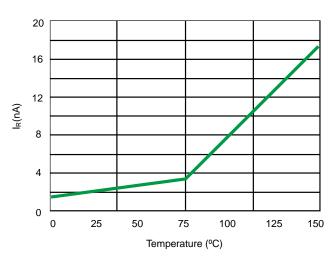


Fig 4. Typical Leakage Current vs. Temperature

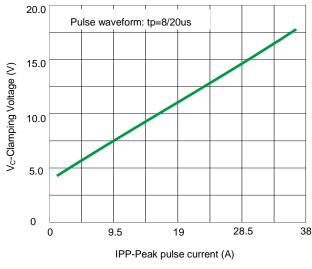


Fig 5. Clamping voltage vs. Peak pulse current

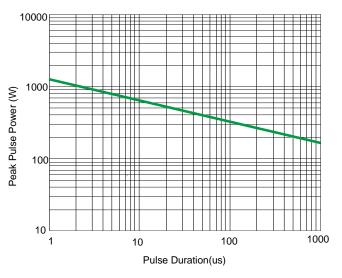
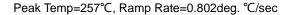
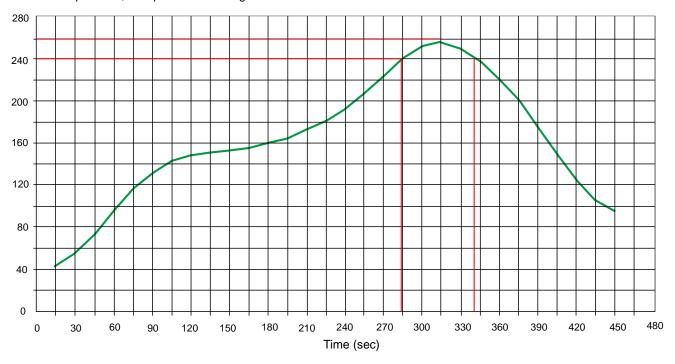


Fig 6. Non-Repetitive Peak Pulse Power vs. Pulse time

### **Solder Reflow Recommendation**



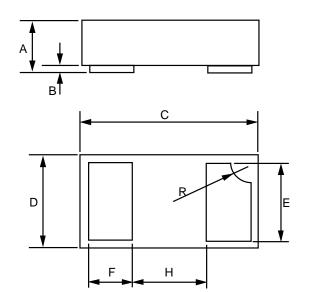


## **PCB** Design

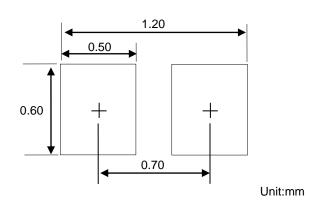
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

# Product dimension (DFN1006-2L)



Dim	Inc	hes	Millimeters		
	MIN	MAX	MIN	MAX	
Α	0.013	0.020	0.34	0.50	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.027	0.55	0.680	
Е	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
Н	0.015Typ.		0.40Тур.		
R	0.001	0.005	0.05	0.15	



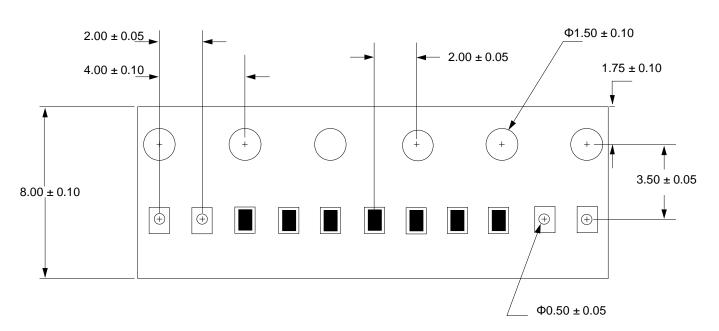
Suggested PCB Layout

# Ordering information

Device	Package	Reel	Shipping
PESDHC2FD4V5BH	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

## **Load with information**





Unit: mm

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